

FIG.1

212 NO. of active lines (M)	214 Division ratio (R)	216 No. of reference clocks per scanning period (N)	218 Frame frequency
220 160	222 1	224 18	226 69.4 Hz
130	1	22 22	69.9 Hz
230 100	232 1	28 28	71.4 Hz
70	232 2	20 20	71.4 Hz
40	4	18	69.4 Hz
10	16	18	69.4 Hz
240 160	1	21	59.5 Hz
130	1	26	59.2 Hz
100	242 1	33 33	60.6 Hz
70	242 2	24 24	59.5 Hz
40	4	21	59.5 Hz
10	16	21	59.5 Hz

FIG.3

Signal name	Meaning	"Low"	"High"
CS	chip select	accessible	inaccessible
RS	register address/data selection	address	data
E	data write/read activation	active	inactive
RW	data write/read selection	write	read
D	interactive data	—	—

FIG.2

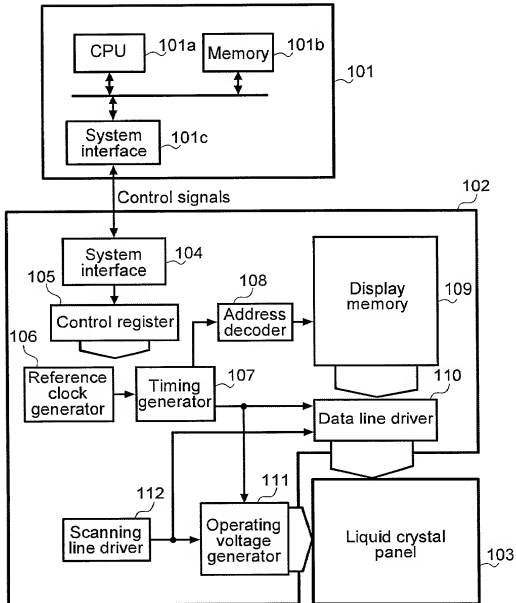


FIG.4

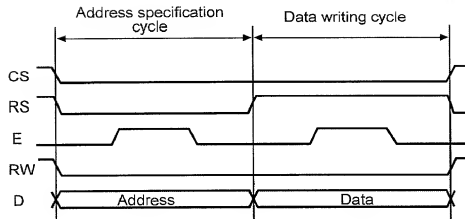


FIG.5

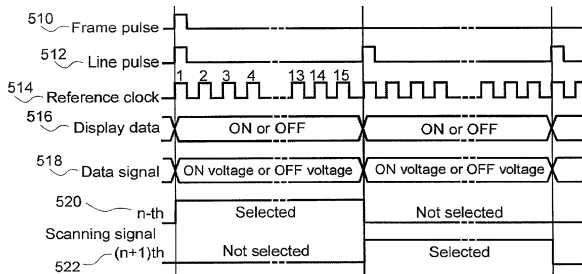


FIG.6

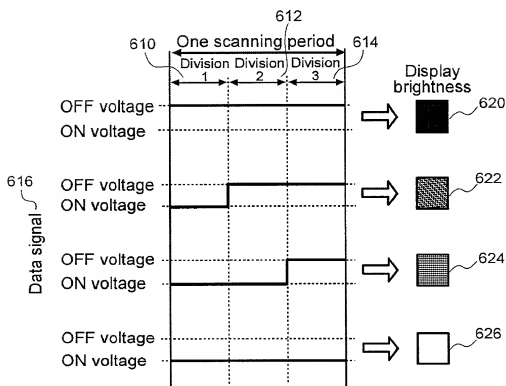


FIG.7

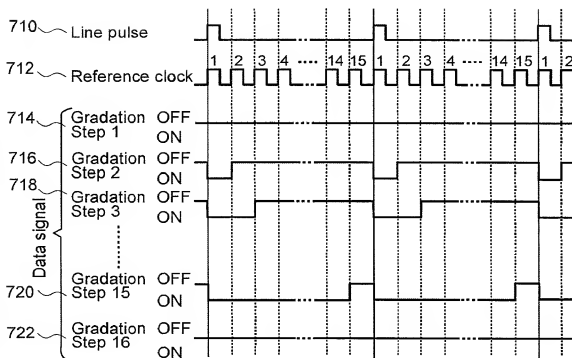


FIG.8

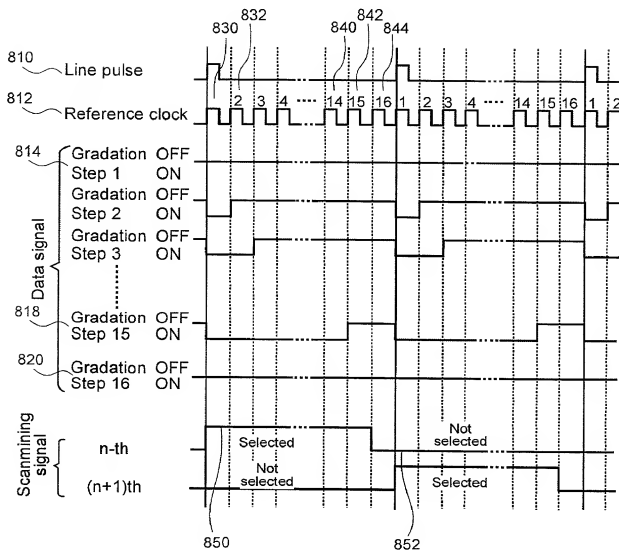


FIG.9

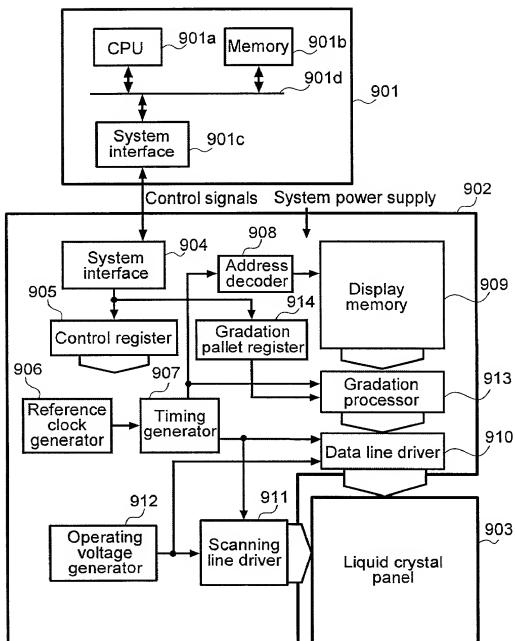


FIG.10

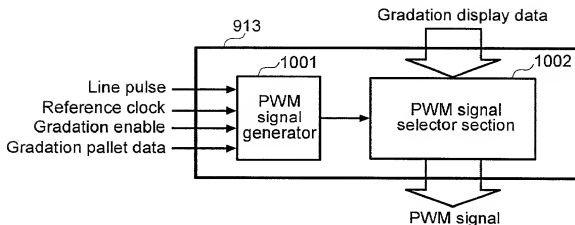


FIG.11

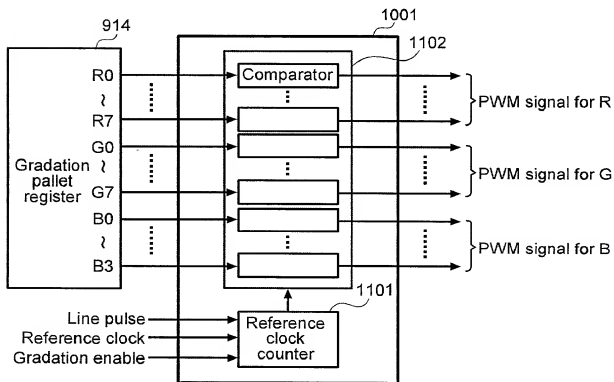


FIG.12

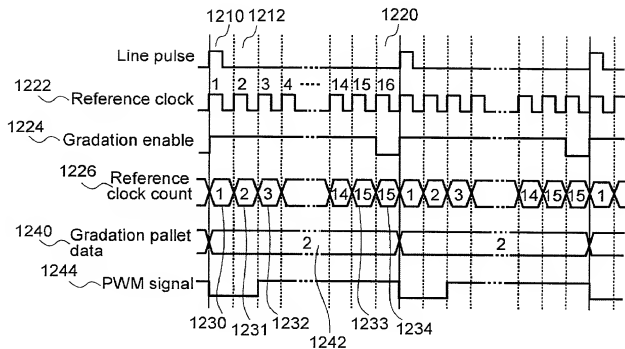


FIG.13

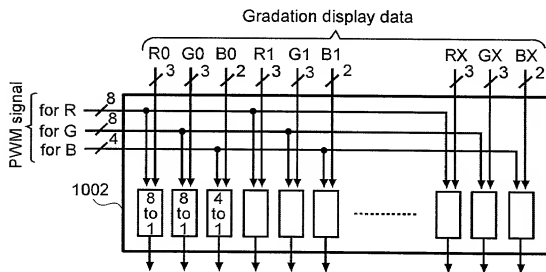


FIG.14

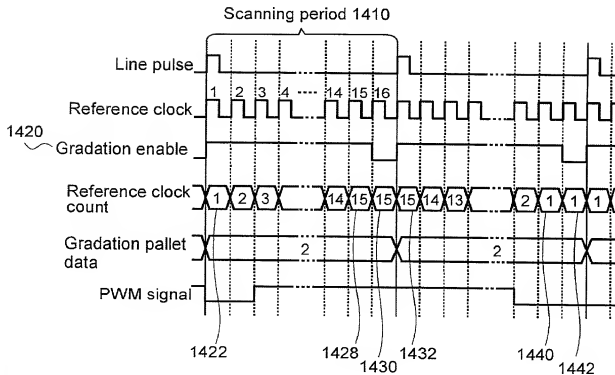


FIG.15a

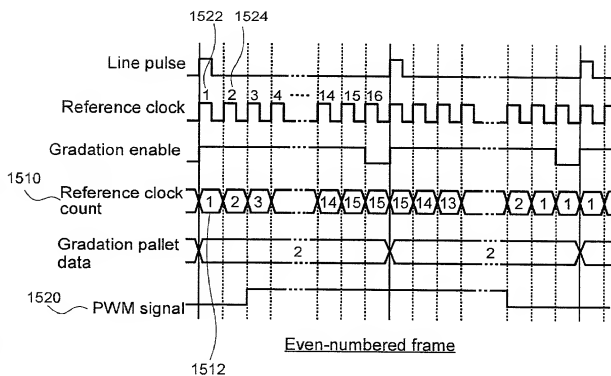


FIG.15b

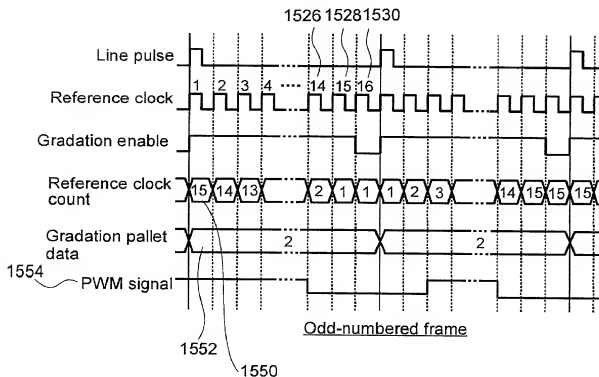


FIG. 16

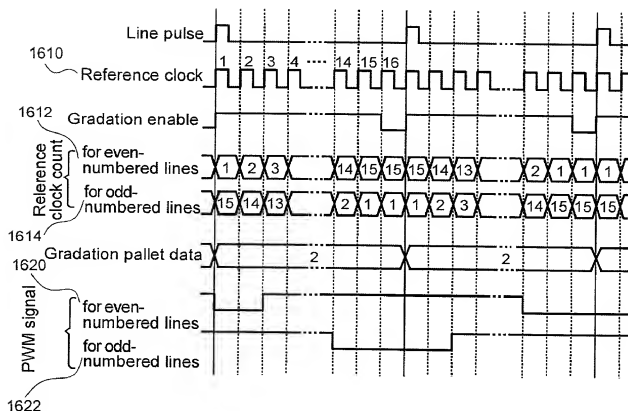


FIG.17

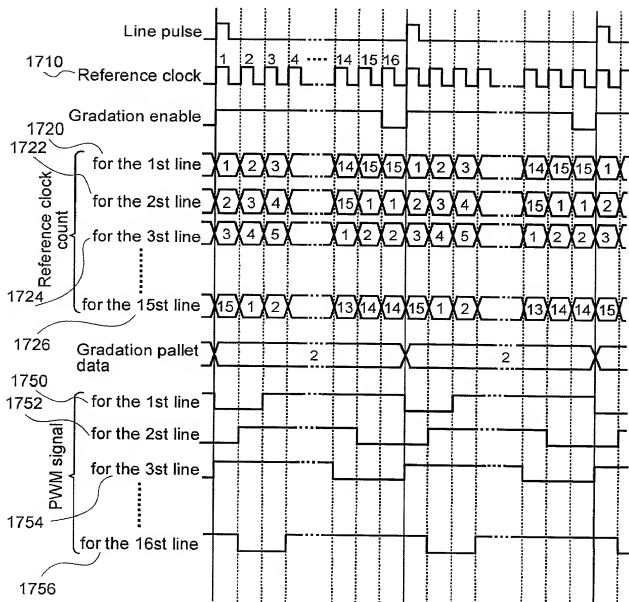


FIG.18a

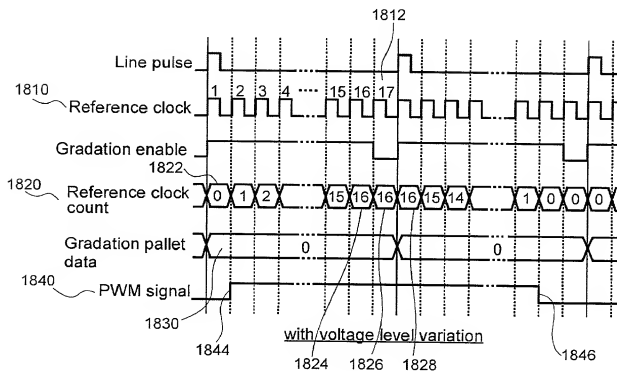


FIG.18b

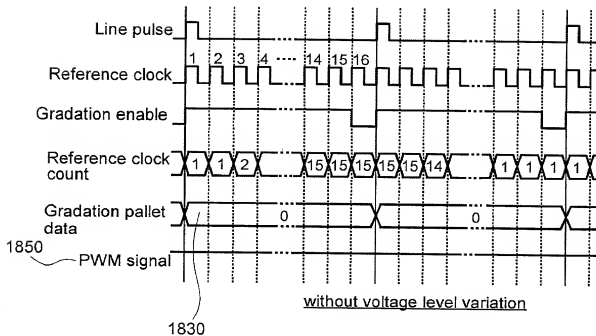


FIG.19

